

3A,40-45V Schottky Barrier Rectifiers

Features

- Low leakage current
- Schottky barrier diodes
- Low forward voltage drop
- Moisture sensitivity: level 1, per J-STD-020
- Halogen-free according to IEC 61249-2-21 definition
- High temperature soldering guaranteed: 260 ℃/10 seconds



eSGB (DO-221AC)

Applications

For use in low voltage, high frequency inverters, free-wheeling and polarity protection application.

Maximum Ratings & Electrical Characteristics(TA=25°C unless otherwise noted)				
Parameter	Symbol	LSL34	LSL345	Unit
Maximum repetitive peak reverse voltage	V _{RRM}	40	45	V
Maximum RMS voltage	V _{RMS}	28	31.5	V
Maximum DC blocking voltage	V _{DC}	40	45	V
Maximum average forward rectified current	I _{F(AV)}	3		Α
Peak forward surge current,8.3ms single half sine-wave superimposed on rated load per diode	Іғѕм	50		A
Operating junction temperature range	TJ	-55 to +150		°C
Storage temperature range	Тѕтс	-55 to +150		°C

Thermal-Mechanical Specifications (TA=25°C unless otherwise noted)				
Parameter	Symbol	Тур	Unit	
Thermal Resistance, Junction to Ambient	R _θ JA	85	°C /W	
Thermal Resistance, Junction to Case	Rejc	15	°C /W	
Thermal Resistance, Junction to Lead	R _{eJL}	18	°C /W	

LSL34 thru LSL345 GOOD-ARK Electronics

Electrical Specifications(TA=25°C unless otherwise noted)						
Parameter	Symbol	Test Conditions	LSL34	LSL345	Unit	
Forward Drop Voltage	V _F	I _F =3A	0.45		V	
Reverse leakage current @V _R	lR	T _J =25°C	0.5		^	
		T」=125°C	10	00	mA	
Typical junction capacitance	Сл	4.0 V 1 MHZ	300		pF	

Note:

1. Mounted on copper pad area of 0.2x0.2" (5.0 x 5.0mm) to each terminal.

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Ratings and Characteristics Curves

(TA = 25°C unless otherwise noted)

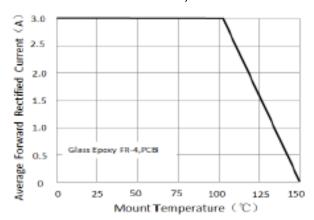


Figure 1.Forward Current Derating Curve

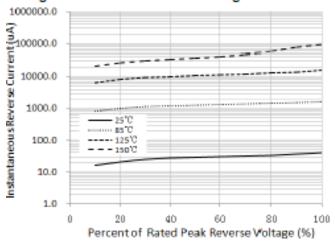


Figure 3. Typical Reverse Characteristics

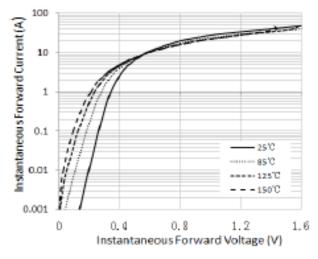


Figure 5. Typical Instantaneous Forward Characteristics

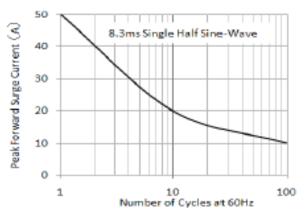


Figure 2.Maximum Non-Repetitive Peak Forward Surge Current

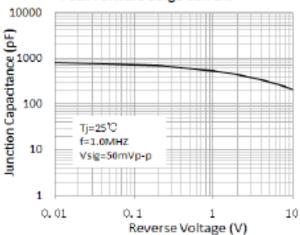


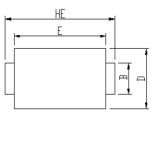
Figure 4. Typical Junction Capacitance



Package Outline Dimensions

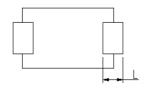
in inches (millimeters)

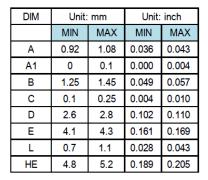
eSGB (DO-221AC)



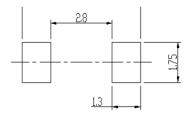








Soldering footprint



Revision History

Document Version	Date of release	Description of changes
Rev.A	2021.06.01	Released Datasheet
Rev.B	2023.10.12	Modify document format
Rev.C	2023.12.29	Modify package name



LSL34 thru LSL345

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